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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Wilson, Allan

Patent No.: 7,154,153

Group Art Unit: 2815

Issue Date: December 26, 2006

Docket No: 303.356US1

Title: MEMORY DEVICE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN: CERTIFICATE OF CORRECTION BRANCH

We are transmitting herewith the attached:

X Authorization to charge Deposit Account 19-0743 in the amount of \$100.00 to cover the Certificate of Correction fee.

- X Request for Certificate of Correction.
- X Certificate of Correction Form PTO-1050 (in duplicate)
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Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer No: 21186

Name: David R. Cochran

Reg. No.46,632 DRC/cmb

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CHRISTOPHER BAYETY

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of Correction



**PATENT** 

## IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,154,153

Docket No:

303.356US1

Issue Date:

December 26, 2006

Patentee: Leonard Forbes et al.

Customer No.: 21186

01106

Confirmation No.: 9876

Title

**MEMORY DEVICE** 

### REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

ATTN: CERTIFICATE OF CORRECTION BRANCH

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

#### Pursuant to 1.20(a), please charge Deposit Account 19-0743 in the amount of \$100.00.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES ET AL.

02/01/2007 BABRAHA2 00000006 190743 7154153

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By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6900

Date: 30 200494

Bv:

David R. Cochran Reg. No: 46,632

DRC/cmb

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 40 day of January 2007

CHRISTOPHER EXPERT

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# UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO

: 7,154,153

Page (1) of 2

DATED

: December 26, 2006

INVENTOR(S)

: Forbes et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents", in column 1, line 1, delete "345/20" and insert - - 345/206 - -, therefor.

On page 3, in field (56), under "Other Publications", in column 2, line 17, delete "Ally" and insert - - Alloy - -, therefor.

On page 4, in field (56), under "Other Publications", in column 1, line 46, delete "anfd" and insert - - and - -, therefor.

On page 5, in field (56), under "Other Publications", in column 2, line 1, delete "Hyberstein" and insert - - Hybertsen - -, therefor.

On page 5, in field (56), under "Other Publications", in column 2, line 11, delete "Electkrical" and insert - - Electrical - -, therefor.

On page 5, in field (56), under "Other Publications", in column 2, line 12, delete "CEII" and insert - - Cell - -, therefor.

On page 5, in field (56), under "Other Publications", in column 2, line 13, delete "Delvice Lettes" and insert - - Device Letters - -, therefor.

In column 10, line 61, after "a" delete "w".

SCHWEGMAN, LUNDBERG, WOESSNER, & KLUTH, P.A.

In column 17, line 54, after "field-effect" insert - - transistor. According to one aspect of the invention, a barrier energy between the floating - -.

In column 19, line 35, in Claim 10, after "claim" delete "2" and insert - - 9 - -, therefor.

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

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Page (2) of 2

**DATED** 

: December 26, 2006

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: Forbes et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 19, line 40, in Claim 11, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 19, line 44, in Claim 12, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 19, line 47, in Claim 13, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 19, line 50, in Claim 14, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 19, line 52, in Claim 15, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 19, line 54, in Claim 16, after "claim" delete "2" and insert - - 9 - -, therefor.

In column 25, line 32, in Claim 38, after "insulator" delete "being less than approximately eV".

In column 27, line 30, in Claim 44, after "eV" delete "," and insert - -; - -, therefor.

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In column 17, line 54, after "field-effect" insert - - transistor. According to one aspect of the invention, a barrier energy between the floating - -.

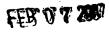
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